Compare Board Receiver

**EC_CHAN**
- CHANNEL
  - VIN+
  - VIN-
  - UNPLUG_THRESH
  - MULT_THRESH

**BR_CHAN**
- CHANNEL
  - VIN+
  - VIN-
  - UNPLUG_THRESH
  - MULT_THRESH

**J1**
- RJ45
  - 123
  - 127
  - 126
  - 128

**BR_MULT_THRESH**

**EC_MULT_THRESH**

**UNPLUG_THRESH**

**BR_VIN+**
- BR_VIN-
- BR_VRCV
- BR_ENABLE
- BR_SUM

**EC_VIN+**
- EC_VIN-
- EC_VRCV
- EC_ENABLE
- EC_SUM

**SHEET=29**

**SHEET=30**

**RJ45**
- 121
- 124
- 122
- 125

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**Compare Board Receiver**

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UNPLUG_THRESH

J2

RJ45

BR_CHAN

EC_CHAN

BR_MULT_THRESH

EC_MULT_THRESH

EC_VIN+

EC_VIN-

EC_VRCV

EC_ENABLE

EC_SUM

BR_VIN+

BR_VIN-

BR_VRCV

BR_ENABLE

BR_SUM

VIN+

VIN-

MULT_THRESH

UNPLUG_THRESH

SUM

VRCV

ENABLE

SHEET=53

SHEET=54

EC_CHAN CHANNEL

BR_CHAN CHANNEL

SHEET=53

SHEET=54

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J2
REN
VIN+, VIN-
MULT_THRESH
UNPLUG_THRESH

RJ45
31
34
32
35
EC_VIN+, EC_VIN-
UNPLUG_THRESH
MULT_THRESH

EC_CHAN
VIN+, VIN-
VRCV
ENABLE
SUM
EC_VRCV
EC_ENABLE
EC_SUM
EC_MULT_THRESH

BR_CHAN
VIN+, VIN-
VRCV
ENABLE
SUM
BR_VRCV
BR_ENABLE
BR_SUM
BR_MULT_THRESH

Compare Board Receiver

EC_CHAN SHEET=56
BR_CHAN SHEET=57

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Compare Board Receiver
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Compare Board Receiver

EC_CHAN
- CHANNEL
- VIN+
- VIN-
- UNPLUG_THRESH
- MULT_THRESH
- SHEET=65

BR_CHAN
- CHANNEL
- VIN+
- VIN-
- UNPLUG_THRESH
- MULT_THRESH
- SHEET=66

UNPLUG_THRESH

J2
- 63
- 67
- 65
- 68

RJ45
- 61
- 64
- 62
- 65

BR_VIN+
- 67
- 65

BR_VIN-
- 68

EC_VIN+
- 64

EC_VIN-
- 62

UNPLUG_THRESH

BR_MULT_THRESH

EC_MULT_THRESH

BR_VRCV

BR_ENABLE

BR_SUM

EC_VRCV

EC_ENABLE

EC_SUM

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Compare Board Receiver

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ATLAS TFW Hardware Interlock Logic Box
Receive Board Input Channel
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